



#6/Election  
for reply  
10/5/02  
PATENT

Attorney Docket No.: SAM-0203

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Dong-Jun Kim, *et al.* Examiner: Nhu, D.  
Serial No.: 10/039,126 Group Art Unit: 2818  
Filing Date: January 3, 2002  
Title: SEMICONDUCTOR DEVICE HAVING A FLASH MEMORY CELL AND  
FABRICATION METHOD THEREOF

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Post Office as First Class Mail in an envelope addressed to the Assistant Commissioner, Washington, D.C., 20231.

10-23-02  
Date

Vanessa Marakas  
Vanessa Marakas

Assistant Commissioner for Patents  
Washington, D.C. 20231

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Restriction Requirement dated October 8, 2002 in the above-referenced patent application, the Applicants hereby elect Group I, including claims 1-17, without traverse.

In connection with the foregoing matter, please charge any additional fees which may be due, or credit any overpayment, to Deposit Account Number 50-1798. A duplicate copy of this letter is provided for this purpose.

Respectfully submitted,

Date: October 23, 2002  
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